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(54) Title: LOW COMPLEXITY DATA DETECTION USING FAST FOURIER TRANSFORM OF CHANNEL CORRELATION MATRIX

(57) Abstract: A combined signal is received over a shared spectrum in a time slot in a time division duplex communication system using code division multiple access. Each data signal experiences a similar channel response. The similar channel response is estimated. A matrix representing a channel of the data signals based on in part the estimated channel response is constructed. A spread data vector is determined based on in part a fast fourier transform (FFT) decomposition of a circulant version of the channel matrix. The spread data vector is despread to recover data from the received combined signal.

[0002] BACKGROUND

[0007] Another approach is single user detection. In single user detection, data is only recovered for a single user (one UE 14<sub>1</sub>). Based on the application, the single user detected data may have been sent using one or multiple codes. Approaches for implementing single user detection include block linear equalization using a Cholesky

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or an approximate Cholesky decomposition. These approaches have a high complexity. The high complexity leads to increased power consumption, which at the UE 14<sub>1</sub> results in reduced battery life. Accordingly, it is desirable to have alternate approaches to detecting received data.

[0008] SUMMARY

[0009] A combined signal is received over a shared spectrum in a time slot in a time division duplex communication system using code division multiple access. Each data signal experiences a similar channel response. The similar channel response is estimated. A matrix representing a channel of the data signals based on in part the estimated channel response is constructed. A spread data vector is determined based on in part a fast fourier transform (FFT) decomposition of a circulant version of the channel matrix. The spread data vector is despread to recover data from the received combined signal.

[0010] BRIEF DESCRIPTION OF THE DRAWING(S)

[0011] Figure 1 is a wireless communication system.  
[0012] Figure 2 is a simplified transmitter and a single user detection receiver.  
[0013] Figure 3 is an illustration of a communication burst.  
[0014] Figure 4 is a flowchart of low complexity data detection.  
[0015] Figures 5-15 are graphs of the performance of low complexity data detection.

[0016] DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENT(S)

[0017] Figure 2 illustrates a simplified transmitter 26 and receiver 28 using low complexity data detection in a TDD/CDMA communication system. In a typical system, a transmitter 26 is in each UE 14<sub>1</sub> to 14<sub>3</sub> and multiple transmitting circuits 26 sending multiple communications are in each base station 12<sub>1</sub> to 12<sub>5</sub>. The low complexity data detector receiver 28 may be at a base station 12<sub>1</sub>, UEs 14<sub>1</sub> to 14<sub>3</sub> or

both. The receiver 28 can be used at a UE 14<sub>1</sub> for either multiuser or single user detection of a medium to high data rate service, such as a 2 megabits per second (Mbs). The receiver 28 can also be used at a base station 12<sub>1</sub>, when only a single UE 14<sub>1</sub> transmits in a time slot.

[0018] The transmitter 26 sends data over a wireless radio channel 30. A data generator 32 in the transmitter 26 generates data to be communicated to the receiver 28. A modulation/spreading sequence insertion device 34 spreads the data and makes the spread reference data time-multiplexed with a midamble training sequence in the appropriate assigned time slot and codes for spreading the data, producing a communication burst or bursts.

[0019] A typical communication burst 16 has a midamble 20, a guard period 18 and two data bursts 22, 24, as shown in Figure 3. The midamble 20 separates the two data bursts 22, 24 and the guard period 18 separates the communication bursts to allow for the difference in arrival times of bursts transmitted from different transmitters 26. The two data bursts 22, 24 contain the communication burst's data.

[0020] The communication burst(s) are modulated by a modulator 36 to radio frequency (RF). An antenna 38 radiates the RF signal through the wireless radio channel 30 to an antenna 40 of the receiver 28. The type of modulation used for the transmitted communication can be any of those known to those skilled in the art, such as quadrature phase shift keying (QPSK) or an N-ary quadrature amplitude modulation (QAM).

[0021] The antenna 40 of the receiver 28 receives various radio frequency signals. The received signals are demodulated by a demodulator 42 to produce a baseband signal. The baseband signal is processed, such as by a channel estimation device 44 and a low complexity data detection device 46, in the time slot and with the appropriate codes assigned to the received bursts. The channel estimation device 44 uses the midamble training sequence component in the baseband signal to provide channel information, such as channel impulse responses. The channel information is

used by the data detection device 46 to estimate the transmitted data of the received communication bursts as hard symbols.

[0022] The data detection device 46 uses the channel information provided by the channel estimation device 44 and the known spreading codes used by the transmitter 26 to estimate the data of the desired received communication burst(s). Low complexity data detection is explained in conjunction with the flowchart of Figure 4. Although low complexity data detection is explained using the third generation partnership project (3GPP) universal terrestrial radio access (UTRA) TDD system as the underlying communication system, it is applicable to other systems. That system is a direct sequence wideband CDMA (W-CDMA) system, where the uplink and downlink transmissions are confined to mutually exclusive time slots.

[0023] The receiver 28 receives using its antenna 40 a total of  $K$  bursts that arrive simultaneously, 48. The  $K$  bursts are superimposed on top of each other in one observation interval. Some or all of the  $K$  bursts may arise from or go to the same users for higher data rate services. For the 3GPP UTRA TDD system, each data field of a time slot corresponds to one observation interval.

[0024] A  $k^{\text{th}}$  burst of the  $K$  bursts uses a code of  $\underline{C}^{(k)}$  of length  $Q$  chips to spread each of its  $N_s$  symbols to yield a sequence of length  $Q \cdot N_s$  chips. The  $k^{\text{th}}$  burst passes through a channel with a known or estimated channel response,  $\underline{h}^{(k)}$ , of length  $W$  chips to form a chip sequence of length,  $N_c = (SF \cdot N_s + W - 1)$ .  $SF$  is the spreading factor. Since uplink signals may originate from multiple UEs 14<sub>1</sub> to 14<sub>3</sub>, each  $\underline{h}^{(k)}$  in the uplink may be distinct. For the downlink in the absence of transmit diversity, all bursts pass through the same channel and have the same  $\underline{h}^{(k)}$ . At the receiver 28, the bursts from all users arrive superimposed as a single received vector,  $\underline{r}$ . Some or all of the  $K$  bursts may be part of a multi-code transmission. The multi-codes have the same  $\underline{h}^{(k)}$ , because they originate from the same transmitter 26.

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[0025] The multi-user signal model consists of  $N_c$  known received chips and  $K \cdot N_s$  unknown information bearing symbols. The symbol response,  $\underline{s}^{(k)}$ , of the  $k^{\text{th}}$  burst is the convolution of  $\underline{C}^{(k)}$  with  $\underline{h}^{(k)}$ . Accordingly,  $\underline{s}^{(k)}$  is of length  $(SF+W-1)$  chips.  $W$  is the impulse response, which represents the trail of chips left by a unity symbol. The  $N_s$  unknown symbols of the  $k^{\text{th}}$  burst form a column vector  $\underline{d}^{(k)}$ .  $\underline{r}^{(k)}$  is the contribution of the  $k^{\text{th}}$  burst to the overall received chip vector,  $\underline{r} = \underline{d}^{(k)}$  is the data vector for the  $k^{\text{th}}$  burst.  $\underline{d}^{(k)}$  and  $\underline{r}^{(k)}$  are related by Equation 1.

$$\underline{r}^{(k)} = A^{(k)} \underline{d}^{(k)}, \text{ where } k = 1 \dots K \quad \text{Equation 1}$$

[0026]  $A^{(k)}$  is the channel response matrix for the  $k^{\text{th}}$  burst, which is an  $N_c \times N_s$  matrix whose  $j^{\text{th}}$  column is the symbol-response of the element of  $\underline{d}^{(k)}$ . Assuming a time-invariant symbol-response, each column of  $A^{(k)}$  has the same support,  $\underline{s}^{(k)}$ , and successive columns are zero-padded and shifted versions of the first column. The overall, chip-rate, received vector is per Equation 2.

$$\underline{r} = \sum_{i=1}^K \underline{r}^{(k)} + \underline{n} \quad \text{Equation 2}$$

$\underline{n}$  is a zero-mean noise vector with independent identical distribution (i.i.d.) components of the variance,  $\sigma^2$ . Equation 2 becomes Equation 3, when written as a single matrix equation.

$$\underline{r} = A \underline{d} + \underline{n} \quad \text{Equation 3}$$

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[0027]  $A$  is the overall channel response matrix, which is a matrix of size  $N_c \times K \cdot N_s$ .  $\underline{d}$  is the data vector, which is a column vector of length  $K \cdot N_s$ . Equation 2 and Equation 3 model the inter-symbol interference (ISI) and multiple-access interference (MAI) in the received vector,  $\underline{r}$ .

[0028] The signal models of Equations 1, 2 and 3 are formulated for chip rate sampling, such as 3.84 Mega chips per second (Mcps) in 3GPP UTRA system. For increased statistical accuracy, a receiver 28 may use over-sampling, such as a multiple chip rate sampling. A typical multiple chip rate sampling is twice the chip rate, although other multiples may be used. When using multiple chip rate sampling, the received signal burst will be over-sampled generating multiple sampled sequences. Each sequence is sampled at the chip rate with different time offsets with respect to one another. The  $k^{\text{th}}$  burst passes through a channel with a known or estimated channel response,  $\underline{h}_m^{(k)}$ , for the  $m^{\text{th}}$  sampled sequence.  $\underline{r}_m^{(k)}$  is the contribution of the  $k^{\text{th}}$  burst to the  $m^{\text{th}}$  overall sampled chip vector,  $\underline{r}_m$ . The data symbol vectors  $\underline{d}^{(k)}$  and the  $m^{\text{th}}$  sampled chip vector  $\underline{r}_m^{(k)}$  are related by Equation 4.

$$\underline{r}_m^{(k)} = A_m^{(k)} \underline{d}^{(k)}, \quad k = 1 \dots K, \quad m = 1 \dots M \quad \text{Equation 4}$$

$A_m^{(k)}$  is the symbol response matrix for the  $m^{\text{th}}$  sequence. It is a matrix of size  $N_c \times N_s$ , whose  $j^{\text{th}}$  column is the  $m^{\text{th}}$  sampled symbol-response of the  $j^{\text{th}}$  element of  $\underline{d}^{(k)}$ .

[0029] Equation 5 is the overall, chip-rate, received vector,  $\underline{r}_m$ , of the  $m^{\text{th}}$  sampled sequence.

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$$\underline{r}_m = \sum_{i=1}^K \underline{r}_m^{(k)} + \underline{n} \quad m = 1 \dots M \quad \text{Equation 5}$$

[0030] For an  $M$  multiple of chip rate sampling, a single matrix expression is per Equation 6.

$$\underline{r}' = A' \underline{d} + \underline{n} \quad \text{Equation 6}$$

[0031]  $\underline{r}'$  is the received signal vector and is defined as per Equation 7.

$$\underline{r}' = \begin{bmatrix} r_1 \\ r_2 \\ \vdots \\ r_M \end{bmatrix} \quad \text{Equation 7}$$

[0032]  $A'$  is defined as per Equation 8.

$$A' = \begin{bmatrix} A_1 \\ A_2 \\ \vdots \\ A_M \end{bmatrix} \quad \text{Equation 8}$$

[0033] Equation 9 is Equation 6 rewritten as a summation form of  $K$  bursts.



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$$\underline{r}' = \sum_{k=1}^K A'^{(k)} \underline{d}^{(k)} + \underline{n} \quad \text{Equation 9}$$

[0034] Equation 9 can be rewritten as Equation 10.

$$\underline{r}' = \sum_{k=1}^K H'^{(k)} C^{(k)} \underline{d}^{(k)} + \underline{n} \quad \text{Equation 10}$$

[0035]  $C^{(k)}$  is code sequence of the  $k^{\text{th}}$  burst.  $H'^{(k)}$  is the channel response for the  $k^{\text{th}}$  sequence, which is defined for  $M$  multiple chip rate sampling per Equation 11.

$$H'^{(k)} = \begin{bmatrix} H_1^{(k)} \\ H_2^{(k)} \\ \vdots \\ H_M^{(k)} \end{bmatrix} \quad \text{Equation 11}$$

[0036] When all the signal bursts in a time slot arise from the same user in the uplink or go to the same user in the downlink, the bursts pass through the same propagation path and, accordingly, the same fading channel. As a result,  $H'^{(k)}$  is the same for all bursts ( $H'^{(k)} = H'^{(j)} = H'_c$ , for all  $k$  and  $j$ ) and is replaced in Equation 10 with  $H'_c$  as per Equation 12.

$$\underline{r}' = H'_c \sum_{k=1}^K C^{(k)} \underline{d}^{(k)} + \underline{n} \quad \text{Equation 12}$$

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[0037] Equation 13 is Equation 12 rewritten as a single matrix expression.

$$\underline{r}' = H_c' C \underline{d} + \underline{n} \quad \text{Equation 13}$$

[0038]  $C$  is the code matrix. For  $M$  chip rate sampling,  $H_c'$  is per Equation 14.

$$H_c' = \begin{bmatrix} H_{c1} \\ H_{c2} \\ \vdots \\ H_{cM} \end{bmatrix} \quad \text{Equation 14}$$

[0039] For an  $m^{\text{th}}$  chip rate sample,  $H_{cm}$  is the channel response for the  $m^{\text{th}}$  sampled sequence. Each  $H_{cm}$ ,  $m = 1 \dots M$ , is determined by the channel estimation device 44, 50. The matrix structure of each  $H_{cm}$  is per Equation 15, 52.

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$$H_{cm} = \begin{bmatrix} h_{m,0} & 0 & & & & & 0 \\ h_{m,1} & h_{m,0} & 0 & & & & \\ h_{m,2} & h_{m,1} & \ddots & & & & \\ \vdots & h_{m,2} & & \ddots & & & \\ \vdots & \vdots & & & \ddots & & \\ h_{m,W-3} & \vdots & & & & \ddots & 0 \\ h_{m,W-2} & h_{m,W-3} & & & & h_{m,0} & 0 \\ h_{m,W-1} & h_{m,W-2} & & & & h_{m,1} & h_{m,0} \\ 0 & h_{m,W-1} & & & & h_{m,2} & h_{m,1} \\ & 0 & \ddots & & & \vdots & h_{m,2} \\ & & 0 & \ddots & & \vdots & \vdots \\ & & & 0 & \ddots & & \vdots \\ & & & & \ddots & h_{m,W-3} & \vdots \\ & & & & & h_{m,W-2} & h_{m,W-3} \\ & & & & & 0 & h_{m,W-1} \\ & & & & & h_{m,W-1} & 0 \\ 0 & & & & & 0 & h_{m,W-1} \end{bmatrix} \quad \text{Equation 15}$$

[0040] The overall signal model of data detection is represented as per Equations 16 and 17.

$$\underline{r}' = H_c' \underline{s} + \underline{n} \quad \text{Equation 16}$$

$$\underline{s} = C \underline{d} \quad \text{Equation 17}$$

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[0041]  $\underline{s}$  is the spread data chip vector.  $C$  is the code vector. One approach to determine  $\underline{s}$  is to use a zero forcing (ZF) solution of Equation 16 as per Equation 18.

$$\underline{s} = \left( H_c'^H H_c' \right)^{-1} H_c'^H \underline{r} \quad \text{Equation 18}$$

[0042]  $H_c'^H$  is the hermitian of  $H_c'$ . Another approach is to use a minimum mean square error (MMSE) solution as per Equation 19.

$$\underline{s} = \left( H_c'^H H_c' + \sigma^2 I \right)^{-1} H_c'^H \underline{r} \quad \text{Equation 19}$$

[0043]  $\sigma^2$  is the noise variance.  $I$  is the identity matrix. After solving either Equation 17 or 18 for  $\underline{s}$ , the solution of Equation 17 is obtained by despreading, as represented by Equation 20, 56.

$$\underline{d} = C^H \underline{s} \quad \text{Equation 20}$$

[0044] The following approaches to solve Equations 18 and 19 for  $\underline{s}$  use a fast fourier transform (FFT) decomposition of either a circulant approximation of the channel correlation matrix,  $R$ , or the channel response matrix,  $H_c'$ , 54. Using either matrix requires an approximation; however, using the channel response matrix,  $H_c'$ , also requires truncation of the last  $W-1$  rows of the matrix to make it square. Accordingly, to eliminate degradation due to truncation, the channel correlation matrix,  $R$ , is preferably used.

[0045] A FFT decomposition of the channel correlation matrix,  $R$ , is performed as follows. For a ZF approach,  $R$  is defined as per Equation 21.

$$R = H_c'^H H_c' = \sum_{m=1}^M H_{cm}^H H_{cm} \quad \text{Equation 21}$$

[0046] For a MMSE approach,  $R$  is defined as per Equation 22.

$$R = H_c'^H H_c' + \sigma^2 I \quad \text{Equation 22}$$

[0047] The structure of the channel correlation matrix,  $R$ , is represented as per Equation 23.

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$$R = \begin{bmatrix} R_0 & R_1 & \dots & R_{W-1} & 0 & 0 & \dots & 0 \\ R_1^* & R_0 & \ddots & R_{W-1} & R_{W-1} & 0 & \ddots & \\ & R_1^* & \ddots & R_1 & R_{W-1} & \ddots & 0 & \\ R_{W-1}^* & & \ddots & R_0 & R_1 & \ddots & 0 & 0 \\ 0 & R_{W-1}^* & & R_1^* & R_0 & \ddots & R_{W-1} & 0 \\ 0 & 0 & \ddots & R_1^* & R_0 & \ddots & R_{W-1} & \vdots \\ \vdots & 0 & \ddots & R_{W-1}^* & & \ddots & R_1 & 0 \\ & & \ddots & 0 & R_{W-1}^* & \ddots & R_0 & R_{W-1} \\ & & & 0 & 0 & R_{W-1}^* & R_1 & R_0 \\ & & & & 0 & 0 & R_1^* & R_0 \\ & & & & & 0 & R_{W-1}^* & R_1 \\ & & & & & & 0 & R_0 \\ 0 & & & & & & & R_1 \\ & & & & & & & R_0 \end{bmatrix} \quad \text{Equation 23}$$

[0048] Equations 18 and 19 are rewritten in terms of  $R$  as per Equations 24 and 25, respectively.

$$\underline{s} = R^{-1} H_c'^H \underline{r} \quad \text{Equation 24}$$

$$R \underline{s} = H_c'^H \underline{r} \quad \text{Equation 25}$$

[0049] The matrix-vector multiplication  $R \underline{s}$  can be viewed as a linear combination of column vectors of the channel correlation matrix,  $R$ , weighted by the corresponding elements of data chip vector  $\underline{s}$ , as per Equation 26.

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$$R \underline{s} = s_1 \underline{g}_1 + s_2 \underline{g}_2 + \dots + s_W \underline{g}_W + s_{W+1} \underline{g}_{W+1} + \dots + s_{N \cdot SF} \underline{g}_{N \cdot SF}$$

Equation 26

[0050]  $\underline{g}_i$  is the  $i^{th}$  column of the channel correlation matrix  $R$ .  $s_i$  is the  $i^{th}$  element of spread data chip vector  $\underline{s}$ .

[0051] By modifying the structure of matrix  $R$ , an optimum circulant matrix approximation of channel correlation matrix,  $R_{cir}$ , can be determined using Equation 27.

$$R_{cir} = \begin{bmatrix} R_0 & R_1 & \dots & R_{W-1} & 0 & 0 & \dots & 0 & 0 & \dots & \vdots & R_1^* \\ R_1^* & R_0 & \ddots & R_{W-1} & 0 & \ddots & \ddots & \ddots & \ddots & \ddots & R_{W-1}^* & \vdots \\ R_1^* & R_1^* & \ddots & R_1 & R_{W-1} & \ddots & 0 & 0 & \ddots & 0 & R_{W-1}^* & \vdots \\ R_{W-1}^* & R_{W-1}^* & \ddots & R_0 & R_1 & \ddots & 0 & 0 & \ddots & 0 & 0 & \vdots \\ 0 & R_{W-1}^* & \ddots & R_1^* & R_0 & \ddots & R_{W-1} & 0 & \ddots & \ddots & \vdots & \vdots \\ 0 & 0 & \ddots & R_1^* & R_0 & \ddots & R_{W-1} & 0 & \ddots & \ddots & 0 & \vdots \\ \vdots & 0 & \ddots & R_{W-1}^* & \ddots & \ddots & R_1 & \ddots & \ddots & 0 & 0 & \vdots \\ 0 & \ddots & \ddots & 0 & R_{W-1}^* & \ddots & R_0 & R_1 & \ddots & R_{W-1} & 0 & \vdots \\ 0 & \ddots & \ddots & 0 & 0 & R_{W-1}^* & R_1^* & R_0 & \ddots & \vdots & R_{W-1} & \vdots \\ R_{W-1}^* & 0 & \ddots & 0 & 0 & \ddots & R_1^* & R_0 & \ddots & R_1 & \vdots & \vdots \\ \vdots & R_{W-1}^* & \ddots & \ddots & 0 & \ddots & R_{W-1}^* & \ddots & \ddots & R_0 & R_1 & \vdots \\ R_1 & \vdots & \ddots & 0 & 0 & \dots & 0 & 0 & R_{W-1}^* & \ddots & R_1^* & R_0 \end{bmatrix}$$

Equation 27

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[0052] The first column,  $\underline{q}$ , has the full non-zero elements without any truncation. The circulant matrix,  $R_{cir}$ , is defined by its first column  $\underline{q}$ . The first column  $\underline{q}$  of circulant matrix,  $R_{cir}$ , is obtained by permuting the  $W^{th}$  column  $\underline{g}_W$  of the channel correlation matrix,  $R$ , using the permutation operator or index vector as defined by Equation 28.

$$\underline{p} = [W : N \cdot Q, 1 : W - 1] \quad \text{Equation 28}$$

[0053] Alternately, a circulant matrix is also defined by the  $W^{th}$  column  $\underline{g}_W$  of channel correlation matrix,  $R$ . In general, any column greater than  $W^{th}$  column may be used with a proper index vector (permutation vector).

[0054] This alternate approximate circulant channel correlation matrix,  $R'_{cir}$ , relates to  $R_{cir}$  per Equation 29.

$$R'_{cir} = R_{cir}(:, \underline{p}) . \quad \text{Equation 29}$$

[0055] The advantage with this approach is that  $\underline{g}_W$  is used directly without permutation. However, the solved spread data chip vector  $\underline{s}$  is required to be inverse permuted by the index vector  $\overline{\underline{p}}$  as per Equation 30.

[0056] By permuting the first row in the previous approach, the need for inverse permuting  $\underline{s}$  is eliminated.

$$\overline{\underline{p}} = [N \cdot SF - W + 2 : N \cdot SF, 1 : N \cdot SF - W + 1] \quad \text{Equation 30}$$



[0057] Equation 31 is the FFT decomposition of matrix  $R_{cir}$ .

$$R_{cir} = D_P^{-1} \Lambda_R D_P \quad \text{Equation 31}$$

[0058]  $D_P$  is the P-point FFT matrix and  $\Lambda_R$  is diagonal matrix, whose diagonal is the FFT of the first column of matrix  $R_{cir}$ .  $\Lambda_R$  is defined as  $\Lambda_R = \text{diag}(D_P \underline{q})$ .

[0059] Using a FFT decomposition of the channel response matrix,  $H_c'$ , is performed as follows. Matched filtering,  $H_c'^H \underline{r}'$ , is represented by Equation 32.

$$H_c'^H \underline{r}' = \sum_{m=1}^M H_{cm}^H \underline{r}_m \quad \text{Equation 32}$$

[0060] The channel response matrix that corresponds to each sampled sequence,  $H_{cm}$ ,  $m=1,2,\dots,M$ , are circulant matrixes. Each matrix can be decomposed into three FFT matrix multiplication as per Equation 33.

$$H_{cm} = D_P^{-1} \Lambda_{H_{cm}} D_P, \quad m=1 \dots M \quad \text{Equation 33}$$

[0061] As a result, the decomposition of the channel response matrix is per Equation 34.

$$H_c'^H \underline{r}' = D_P^{-1} \sum_{m=1}^M \Lambda_{H_{cm}}^* D_P \underline{r}_m \quad \text{Equation 34}$$

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[0062] To recover the data chip vector  $\underline{s}$ , Equation 35 is used.

$$\underline{s} = R_{cir}^{-1} H_c'^H \underline{r}' = D_p^{-1} \Lambda_R^{-1} \sum_{m=1}^M \Lambda_{H_{c_m}}^* D_p \underline{r}_m \quad \text{Equation 35}$$

[0063] In the frequency domain, Equation 35 becomes Equation 36.

$$F(\underline{s}) = \frac{\sum_{m=1}^M F(\underline{h}_m)^* \otimes F(\underline{r}_m)}{F(\underline{q})} \quad \text{Equation 36}$$

[0064]  $\otimes$  represents the operation of element by element multiplication. Using Equation 36,  $F(\underline{s})$  is determined. By taking the inverse transform of  $F(\underline{s})$ , the spread data vector,  $\underline{s}$ , is determined. If used for multi-user detection in the downlink or a single user solely uses one time slot in the uplink,  $\underline{s}$  is despread by using all of the codes to recover the transmitted data  $\underline{d}$  as soft symbols. If used for single user detection in the downlink,  $\underline{s}$  is despread using that user's codes to recover that user's data as soft symbols. Hard decisions are made to convert the soft symbols to hard symbols.

[0065] Two approaches to implement the FFT composition are a prime factor algorithm (PFA) and a radix-2 algorithm. Although a PFA is considered more efficient than a radix-2 algorithm when a non-power-of-two number of FFT points is used, the following complexity analysis is based on a radix-2 FFT implementation for simplicity. The complexity based on radix-2 algorithm can be considered as the worst case. Additional improvement in complexity is obtainable when PFA is used. Zero-

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padding radix-2 FFT implementation entails the zero-padding the first column of  $H_{cm}$ ,  $m = 1 \dots M$ , the vectors  $\underline{r}_m, m = 1 \dots M$  and  $\underline{q}$ . The zero-padding makes their length equal to the nearest radix-2 integer that is greater than or equal to the length of a data field. For example, the length of a data field is 976 chips for burst type 1 in a TDD burst specified by 3GPP W-CDMA standard. The nearest radix-2 integer of 976 is 1024 ( $P = 1024$ ).  $P$  is the radix-2 integer.

[0066] Four types of radix-2 FFT computations are required:

$D_P \underline{r}_m$ ,  $D_P \underline{h}_m$ ,  $D_P \underline{g}_1$  and  $\frac{D_P(\cdot)}{P}$ . Two of the computations are computed  $M$  times for

all sampled sequences:  $D_P \underline{r}_m, m = 1 \dots M$  and  $D_P \underline{h}_m, m = 1 \dots M$ . The other two

are performed only once for the sampled sequences.  $D_P \underline{h}_m, m = 1 \dots M$  and  $D_P \underline{g}_1$  are

computed once per time slot.  $D_P \underline{r}_m, m = 1 \dots M$ ,  $\frac{D_P(\cdot)}{P}$  are computed twice per time

slot. As a result, a total of  $3(M + 1)$  radix-2 FFT computations are required. Each needs  $P \log_2 P$  complex operations. By assuming each complex operation requires four real operations, the complexity for radix-2 FFT computations in terms of million real operations per second (MROPS) is per Equation 37.

$$C_1 = 3(M + 1) P \log_2 P \cdot 4 \cdot 100 \cdot 10^{-6} \quad \text{MROPS} \quad \text{Equation 37}$$

[0067] For the complexity of the vector multiplications, there are  $M$  element-to-element vector multiplications and one element-to-element vector division, which are performed twice per time slot. As a result, the complexity for the vector operations in terms of MROPS is per Equation 38.

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$$C_2 = 2(M+1)P \cdot 4 \cdot 100 \cdot 10^{-6} \quad \text{MROPS} \quad \text{Equation 38}$$

[0068] For the complexity of calculating the vector  $\underline{q}$ , it requires  $MW^2$  complex operations, which are performed once per time slot. The complexity in terms of MROPS is per Equation 39.

$$C_3 = MW^2 \cdot 4 \cdot 100 \cdot 10^{-6} \quad \text{MROPS} \quad \text{Equation 39}$$

[0069] The total complexity except for the despreading in MROPS is per Equation 40.

$$C_{ffr} = C_1 + C_2 + C_3 \quad \text{MROPS} \quad \text{Equation 40}$$

[0070] Despreading is performed twice per time slot. The complexity of despreading in terms of MROPS is per Equation 41.

$$C_{desp} = 2 \cdot K \cdot N \cdot Q \cdot 4 \cdot 100 \cdot 10^{-6} \quad \text{MROPS} \quad \text{Equation 41}$$

[0071] As a result, the total complexity of the data detection including despreading is per Equations 42 or 43.

$$C_{Total} = C_{ffr} + C_{desp} \quad \text{MROPS} \quad \text{Equation 42}$$

$$C_{Total} = [3(M+1)P \log_2 P + 2(M+1)P + MW^2 + 2KNQ] \cdot 4 \cdot 100 \cdot 10^{-6} \quad \text{MROPS}$$

Equation 43

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[0072] The following tables show the complexity in MROPS for a 1024-point radix-2 ( $P = 1024$ ) computation. Complexity is shown in Tables 1 at the chip rate and at Table 2 at twice the chip rate sampling. A complexity comparison is made in MROPS between BLE-JD using approximate Cholesky decomposition and low complexity data detection, as shown in Tables 3 and 4. Table 5 is a complexity comparison showing the complexity of low complexity data detection as a percentage of the complexity of BLE-JD using approximate Cholesky decomposition. As shown, low complexity data detection has a much lower complexity than approximate Cholesky based BLE-JD. Depending on the number of bursts transmitted and spreading factors, for most cases, low complexity data detection is 25% at the chip rate, and 30% at twice the chip rate, of the complexity of approximate Cholesky based BLE-JD.

[0073] Table 1. MROPS of a full-burst using low complexity data detection for burst type 1 at chip rate sampling.

# of bursts, $K$	Funcs Executed once per burst			Funcs executed twice per half-burst			Total
	Construct $\underline{q}$	Compute $D_P \underline{h}_m$ $m = 1 \dots M$ Via Radix-2 FFT	Compute $D_P \underline{g}_1$ Via Radix-2 FFT	Compute $D_P \underline{L}_m$ $m = 1 \dots M$ Via Radix-2 FFT	Compute $\frac{D_P(\cdot)}{P}$ Via Radix-2 FFT	Despread $C^{(k)u} \underline{s}$	
1	1.3	4.1	4.1	8.2	8.2	0.78	26.7
8	1.3	4.1	4.1	8.2	8.2	6.25	32.2
12	1.3	4.1	4.1	8.2	8.2	9.4	35.3
13	1.3	4.1	4.1	8.2	8.2	10.1	36
14	1.3	4.1	4.1	8.2	8.2	10.9	36.8
16	1.3	4.1	4.1	8.2	8.2	12.5	38.4

[0074] Table 2. MROPS of a full-burst using low complexity data detection for burst type 1 and twice the chip rate sampling.

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# of bursts, $K$	Funcs Executed once per burst			Funcs executed twice per half-burst			Total
	Construct $\underline{q}$	Compute $D_P \underline{h}_m$ $m = 1 \dots M$ Via Radix-2 FFT	Compute $D_P \underline{g}_1$ Via Radix-2 FFT	Compute $D_P \underline{r}_m$ $m = 1 \dots M$ Via Radix-2 FFT	Compute $\frac{D_P(\cdot)}{P}$ Via Radix-2 FFT	Despread $C^{(k)''} \underline{s}$	
1	2.6	8.2	8.2	16.4	16.4	0.78	52.6
8	2.6	8.2	8.2	16.4	16.4	6.25	58.1
12	2.6	8.2	8.2	16.4	16.4	9.4	61.2
13	2.6	8.2	8.2	16.4	16.4	10.1	61.9
14	2.6	8.2	8.2	16.4	16.4	10.9	62.7
16	2.6	8.2	8.2	16.4	16.4	12.5	64.3

[0075] Table 3. Comparison in MROPS between BLE-JD (approximate Cholesky decomposition) and low complexity data detection at chip rate sampling.

Spreading Factor, Q	# of bursts, $K$	Proposed algorithm	BLE-JD
1	1	26.7	318.2
16	8	32.2	81.1
	12	35.3	174.6
	13	36	205.5
	14	36.8	239.4
	16	38.4	318.2

[0076] Table 4. Comparison in MROPS between BLE-JD (approximate Cholesky decomposition) and low complexity data detection at twice the chip rate sampling.

Spreading Factor, Q	# of bursts, $K$	Proposed algorithm	BLE-JD
1	1	52.6	427.6
16	8	58.1	124.8
	12	61.2	248.3
	13	61.9	287.7
	14	62.7	330.4
	16	64.3	427.6

[0077] Table 5. Complexity of FFT of the channel correlation matrix as a percentage of the complexity of approximate Cholesky based BLE-JD. Approximate Cholesky based BLE-JD is set at 100% complexity.

Spreading Factor, $Q$	# of bursts, $K$	Chip rate sampling	Twice the chip rate sampling
1	1	8%	12%
16	8	39%	47%
	12	20%	25%
	13	18%	22%
	14	15%	19%
	16	12%	15%

[0078] Figures 5-15 are graphs of the performance of low complexity data detection. Two high data rate services are simulated. One is single-code transmission with  $SF = 1$  and the other is multi-code transmission with twelve codes and spreading factor 16 for each. Low complexity data detection is tested under various delay spread types including 3GPP working group four (WG4) defined delay spread channel cases 1, 2 and 3. The simulations are set for both chip rate and twice the chip rate sampling. The length of delay spread is assumed  $W = 57$ . Zero timing error is assumed through the whole simulations. The channel impulse response is assumed to be exactly known. In general, the bit error rate (BER) performance of the multi-code case is better than its corresponding single-code counterpart in the simulation. For the particular example used in the simulation, single-code transmission uses 16 resource units per time slot while the multi-code transmission uses only 12 resource units in each time slot. Using only 12 codes produces less interference and therefore better BER. As compared with BLE-JD, only little or limited performance degradation are observed for proposed algorithm based on FFT decomposition of the channel correlation matrix (FFT-R) in both single-code and multi-code cases. In single-code case, the FFT-R based approach is identical to the block linear equalization structure. The proposed FFT-R based approach and the approach based on FFT of the channel response matrix (FFT-H) are identical to each other at the chip rate sampling.

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[0079] The performance of low complexity data detection using FFT-R and FFT-H is compared to an ideal single user bond, a worst case matched filtering, BLE-JD and single user detection with BLE using an approximate Cholesky decomposition. For the working points of interest, the BER range was typically between 1% and 10%. Only a little or limited signal to noise ratio (SNR) performance degradations are observed for low complexity data detection as compared with BLE-JD, and significant SNR performance enhancement over matched filtering (MF). Low complexity data detection also performs well in an additive white gaussian noise (AWGN) channel environment. Figures 5-15 show that low complexity data detection offers very comparable performance in BER or SNR at much lower complexity and power consumption as compared to BLE-JD using approximate Cholesky decomposition.

\*

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## CLAIMS

What is claimed is:

1. A method for use in receiving a plurality of data signals transmitted over a shared spectrum in a time slot in a time division duplex communication system using code division multiple access, each data signal experiencing a similar channel response, the method comprising:

receiving a combined signal over the shared spectrum in the time slot, the combined signal comprising the plurality of data signals;

sampling the combined signal at a multiple of a chip rate of the combined signal;

estimating the similar channel response;

constructing a channel response matrix based on in part the estimated channel response;

determining a spread data vector based on in part a fast fourier transform (FFT) decomposition of a circulant version of the channel response matrix; and

despreading the spread data vector to recover data from the received combined signal.

2. The method of claim 1 wherein the multiple of chip rate is twice the chip rate.

3. The method of claim 1 wherein the determining is performed using a zero forcing algorithm.

4. The method of claim 1 wherein the determining is performed using a minimum mean square error algorithm.

5. The method of claim 1 for use in downlink multiuser detection wherein the despreading is performed using all codes used in the time slot.

6. The method of claim 1 for use in single user detection wherein the despreading is performed using codes associated with a single user in the time slot.

7. The method of claim 6 wherein the single user detection is uplink single user detection and the single user is an only user transmitting in the time slot.

8. A method for use in receiving a plurality of data signals transmitted over a shared spectrum in a time slot in a time division duplex communication system using code division multiple access, each data signal experiencing a similar channel response, the method comprising:

receiving a combined signal over the shared spectrum in the time slot, the combined signal comprising the plurality of data signals;

estimating the similar channel response;

constructing a channel correlation matrix based on in part the estimated channel response;

determining a spread data vector based on in part a fast fourier transform (FFT) decomposition of a circulant version of the channel correlation matrix; and

despreading the spread data vector to recover data from the received combined signal.

9. The method of claim 8 wherein the combined signal is sampled at a multiple of a chip rate of the combined signal prior to the steps of estimating and constructing.

10. The method of claim 9 wherein the multiple of the chip rate is twice the chip rate.

11. The method of claim 8 wherein the combined signal is sampled at a chip rate of the combined signal prior to the steps of estimating and constructing.

12. The method of claim 8 wherein the FFT decomposition is performed using a permuted first row of the channel correlation matrix.

13. The method of claim 8 wherein the FFT decomposition is performed using a defining row of the channel correlation matrix.

14. The method of claim 8 wherein the determining is performed using a zero forcing algorithm.

15. The method of claim 8 wherein the determining is performed using a minimum mean square error algorithm.

16. The method of claim 8 for use in downlink multiuser detection wherein the despreading is performed using all codes used in the time slot.

17. The method of claim 8 for use in single user detection wherein the despreading is performed using codes associated with a single user in the time slot.

18. The method of claim 17 wherein the single user detection is uplink single user detection and the single user is an only user transmitting in the time slot.

19. A receiver for use in a time division duplex communication system using code division multiple access, the system communicating using a plurality of data signals in a time slot, each data signal experiencing a similar channel response, the receiver comprising:

an antenna for receiving radio frequency signals including the plurality of data signals;

a demodulator for demodulating radio frequency signals to produce a baseband signal;

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a channel estimation device for estimating the similar channel response at a multiple of a chip rate of the combined signal; and

a data detector device for constructing a channel response matrix representing a channel of the data signals based on in part the estimated channel response, determining a spread data vector based on in part a fast fourier transform (FFT) decomposition of a circulant version of the channel response matrix, and despreading the spread data vector to recover data from the received combined signal.

20. The receiver of claim 19 wherein the multiple of the chip rate is twice the chip rate.

21. A receiver for use in a time division duplex communication system using code division multiple access, the system communicating using a plurality of data signals in a time slot, each data signal experiencing a similar channel response, the receiver comprising:

an antenna for receiving radio frequency signals including the plurality of data signals;

a demodulator for demodulating radio frequency signals to produce a baseband signal;

a channel estimation device for estimating the similar channel response; and

a data detector device for constructing a channel correlation matrix representing a channel of the data signals based on in part the estimated channel response, determining a spread data vector based on in part a fast fourier transform (FFT) decomposition of a circulant version of the channel correlation matrix, and despreading the spread data vector to recover data from the received combined signal.

22. The receiver of claim 21 wherein the combined signal is sampled at a multiple of a chip rate of the combined signal and the sampled combined signal is input into the channel estimation and data detector device.

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23. The receiver of claim 22 wherein the multiple of the chip rate is twice the chip rate.

24. The receiver of claim 21 wherein the combined signal is sampled at a chip rate of the combined signal and the sampled combined signal is input into the channel estimation and data detection device.

25. The receiver of claim 21 wherein the FFT decomposition is performed using a permuted first row of the channel correlation matrix.

26. The receiver of claim 21 wherein the FFT decomposition is performed using a defining row of the channel correlation matrix.

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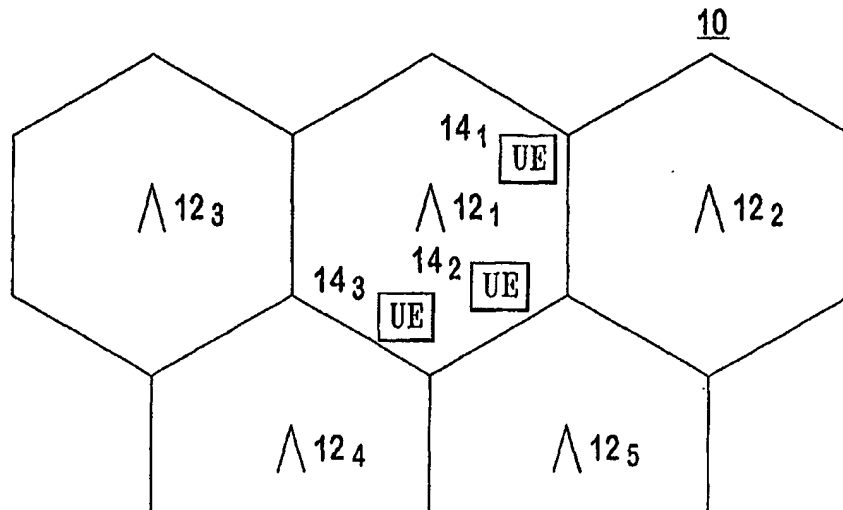


FIG. 1

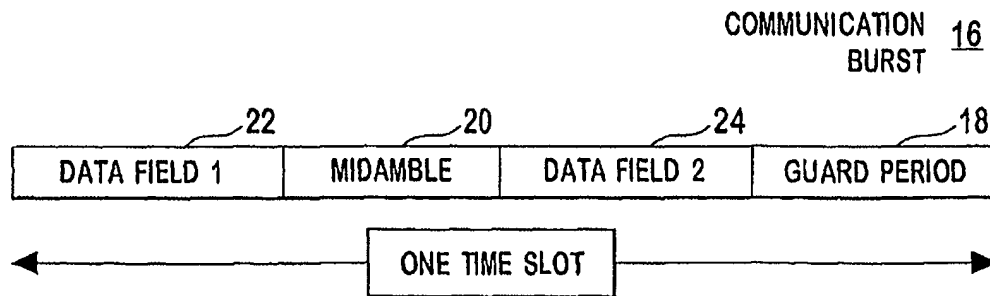


FIG. 3

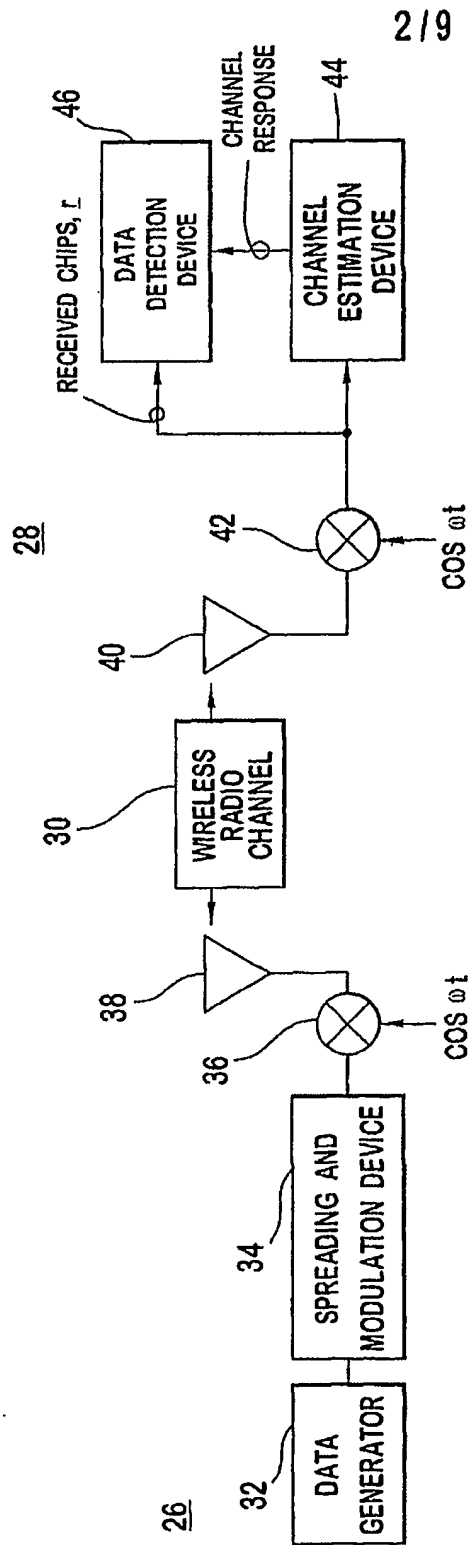


FIG. 2

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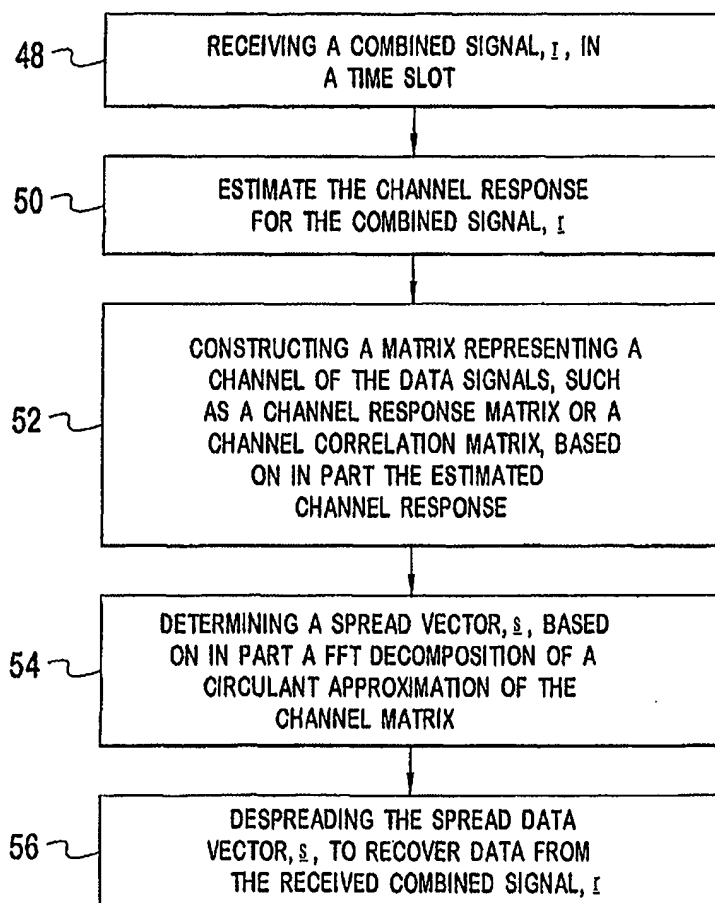


FIG. 4



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FIG. 5

tddWg4Case1, commonCh, EqualAvgPwr, 12 codes with txGrpSize of 1

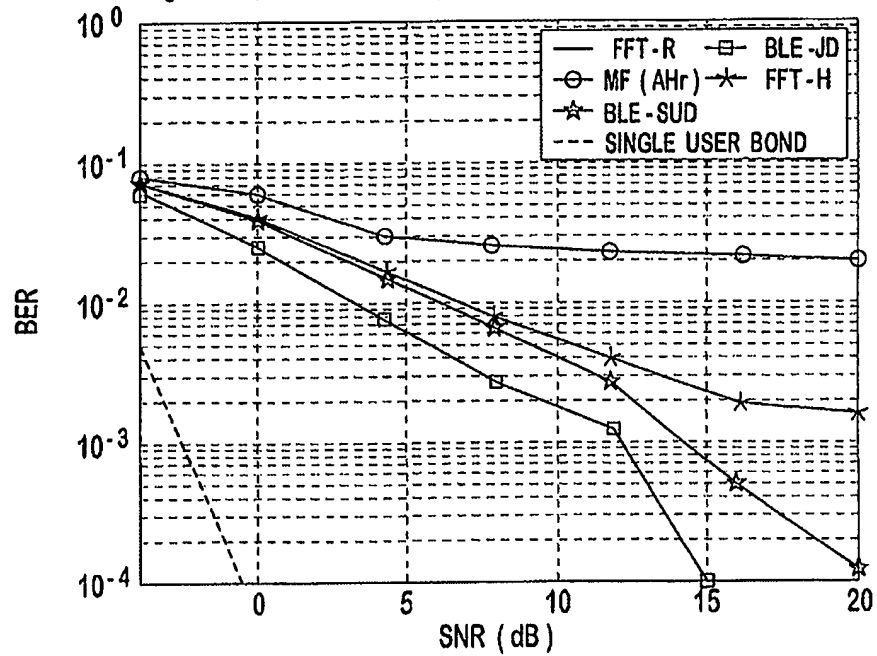
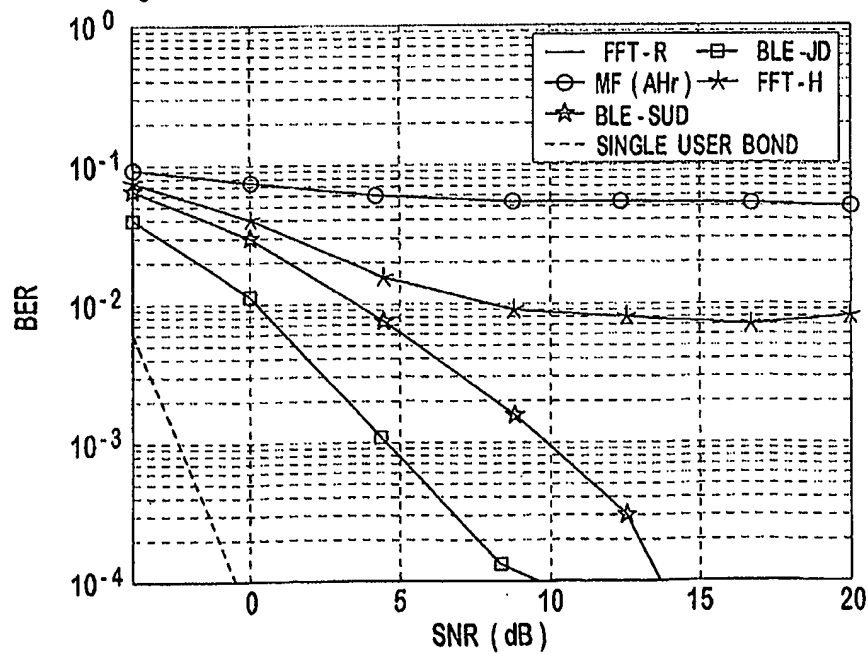


FIG. 6

tddWg4Case2, commonCh, EqualAvgPwr, 12 codes with txGrpSize of 1



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FIG. 7

tddWg4Case3, commonCh, EqualAvgPwr, 12 codes with txGrpSize of 1

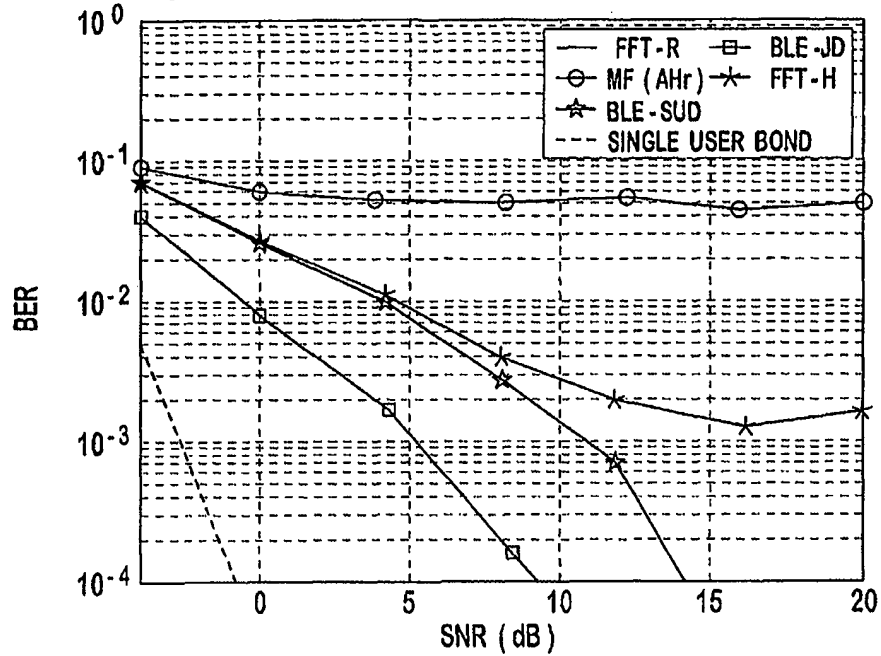
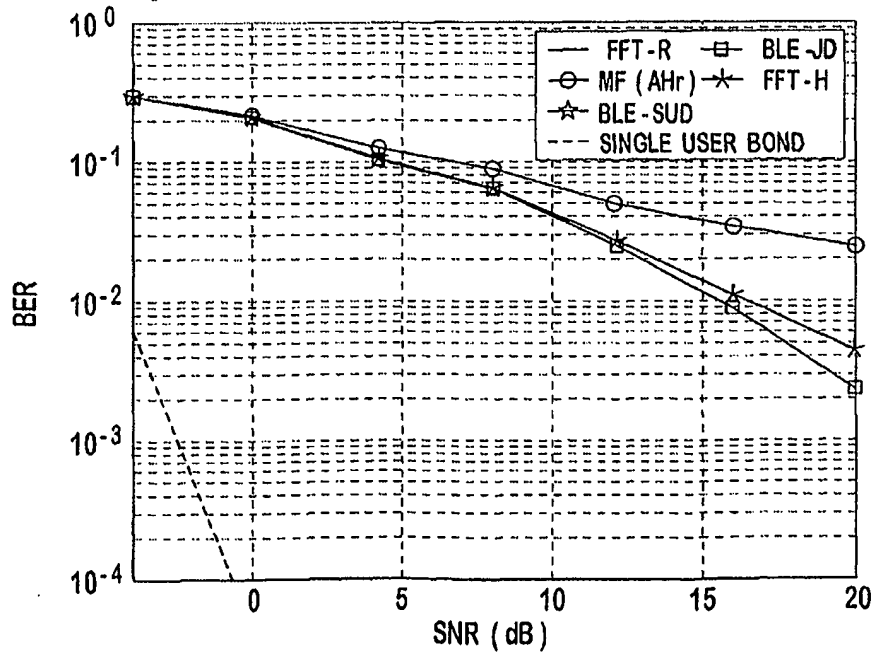


FIG. 8

tddWg4Case1, commonCh, EqualAvgPwr, 1 codes with txGrpSize of 1



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FIG. 9

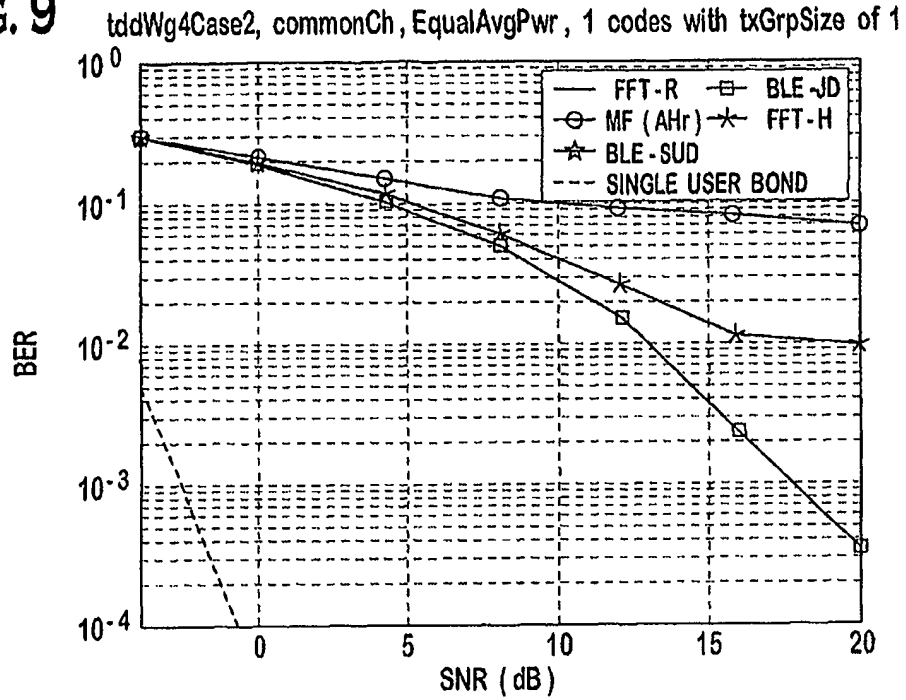
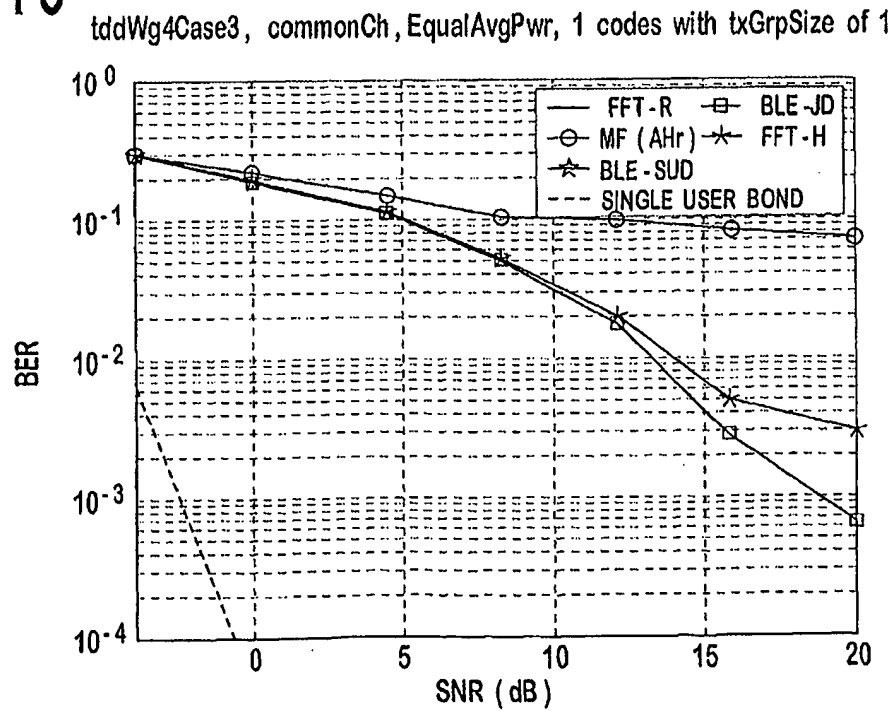


FIG. 10



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FIG. 11

Over-Sampled, tddWg4Case1, 8 codes

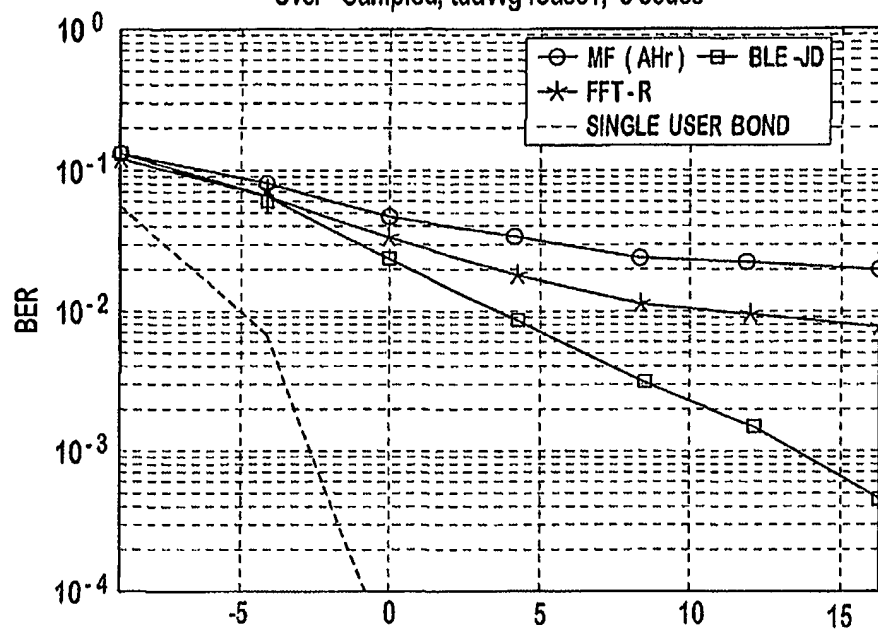
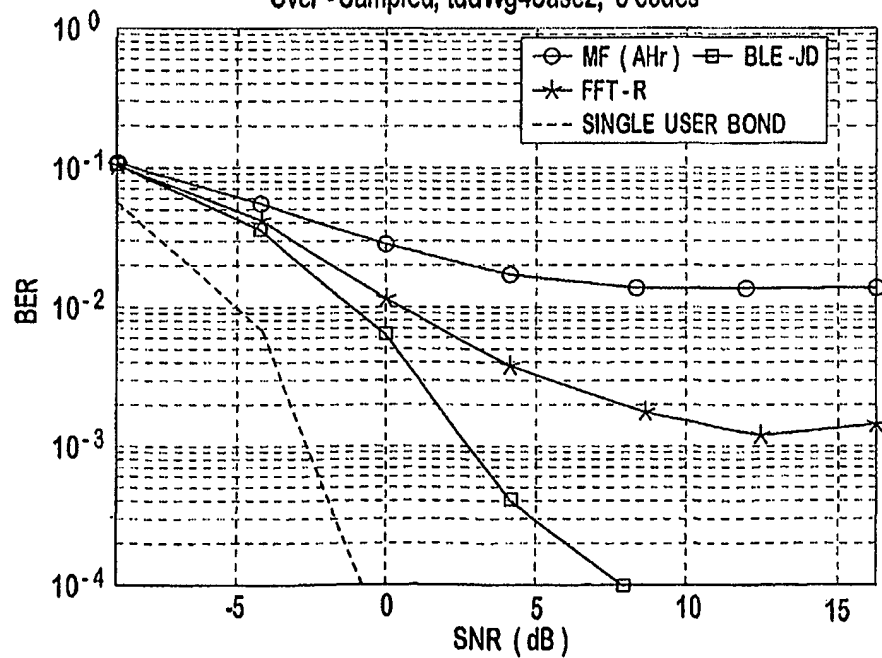
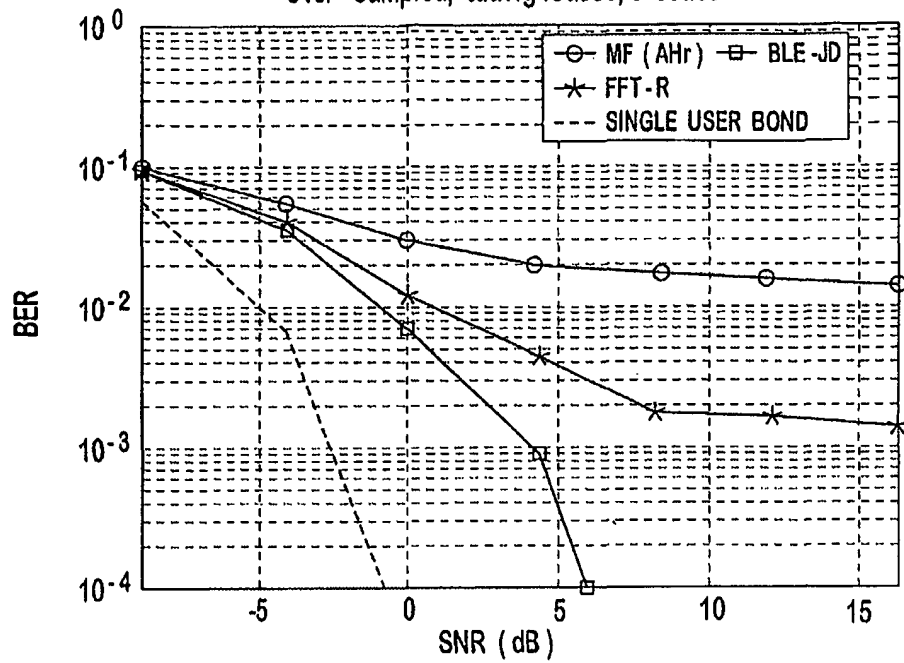


FIG. 12

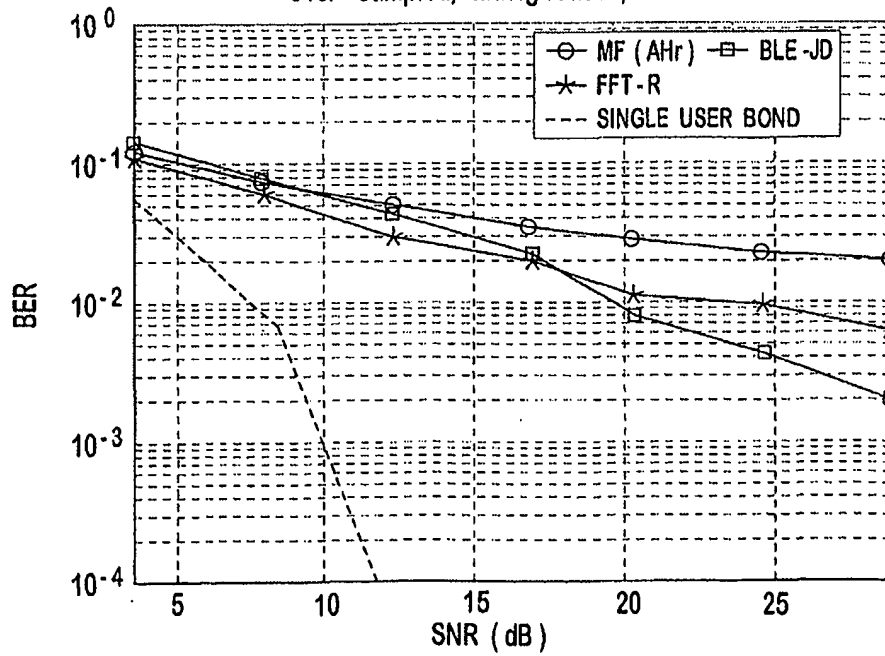
Over-Sampled, tddWg4Case2, 8 codes



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**FIG. 13**  
Over-Sampled, tddWg4Case3, 8 codes

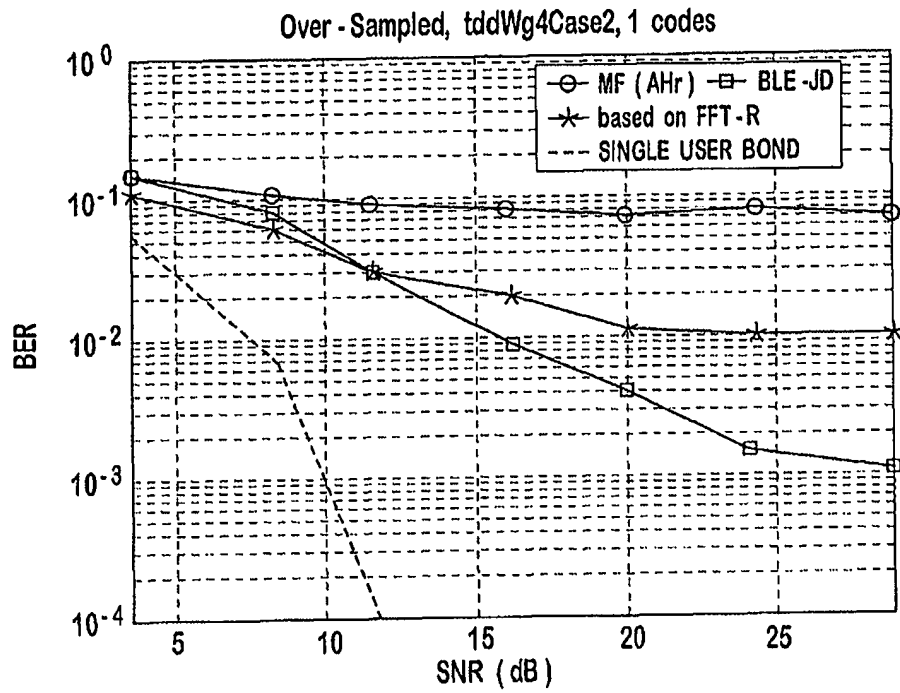


**FIG. 14**  
Over-Sampled, tddWg4Case1, 1 codes



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FIG. 15



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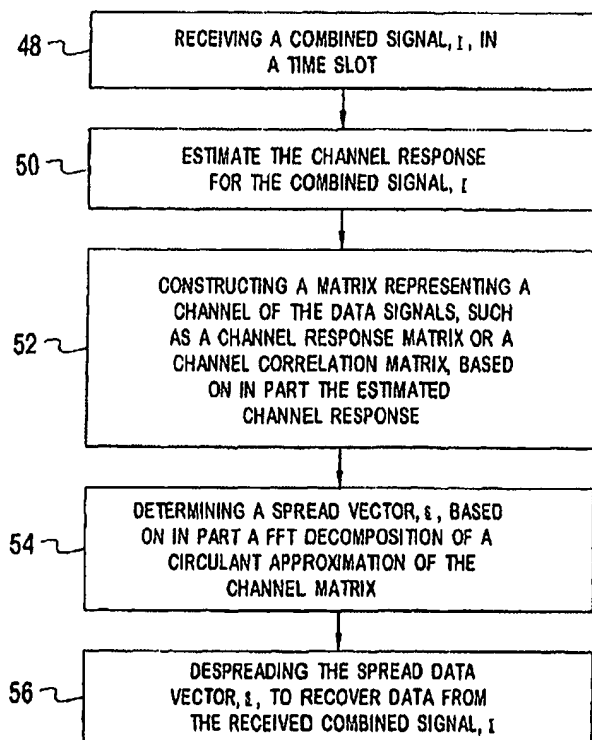
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[Continued on next page]

(54) Title: LOW COMPLEXITY DATA DETECTION USING FAST FOURIER TRANSFORM OF CHANNEL CORRELATION MATRIX



(57) Abstract: A combined signal is received over a shared spectrum in a time slot in a time division duplex communication system using code division multiple access. Each data signal experiences a similar channel response. The similar channel response is estimated. A matrix representing a channel of the data signals based on in part the estimated channel response is constructed. A spread data vector is determined based on in part a fast fourier transform (FFT) decomposition of a circulant version of the channel matrix. The spread data vector is despread to recover data from the received combined signal.

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PCT/US 02/02400

**A. CLASSIFICATION OF SUBJECT MATTER**  
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According to International Patent Classification (IPC) or to both national classification and IPC

**B. FIELDS SEARCHED**

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IPC 7 H04B

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal, PAJ, WPI Data

**C. DOCUMENTS CONSIDERED TO BE RELEVANT**

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y	BENVENUTO N ET AL: "JOINT DETECTION WITH LOW COMPUTATIONAL COMPLEXITY FOR HYBRID TD-CDMA SYSTEMS" VTC 1999-FALL. IEEE VTS 50TH. VEHICULAR TECHNOLOGY CONFERENCE. GATEWAY TO THE 21ST. CENTURY COMMUNICATIONS VILLAGE. AMSTERDAM, SEPT. 19 - 22, 1999, IEEE VEHICULAR TECHNOLOGY CONFERENCE, NEW YORK, NY: IEEE, US, vol. 1 CONF. 50, September 1999 (1999-09), pages 618-622, XP000928922 ISBN: 0-7803-5436-2	1-7,9, 10,20, 22,23
X	page 619, left-hand column, paragraph 3  page 619, right-hand column, paragraph III page 620, left-hand column, paragraph IV page 621, left-hand column, paragraph V  --- -/--	8,11-19, 21,24-26

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Date of the actual completion of the international search

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# INTERNATIONAL SEARCH REPORT

national Application No  
PCT/US 02/02400

C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT		
Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y	<p>WO 99 40698 A (MOTOROLA INC) 12 August 1999 (1999-08-12)</p> <p>abstract page 7, line 18 -page 8, line 7 -----</p>	<p>1-7,9, 10,20, 22,23</p>

# INTERNATIONAL SEARCH REPORT

Information on patent family members

national Application No

PCT/US 02/02400

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WO 9940698	A	12-08-1999	US 6339612 B1	15-01-2002
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			JP 2002503057 T	29-01-2002
			WO 9940698 A1	12-08-1999
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